









PAD1

PAD2

PAD3

PAD4

**JP3: Power Source Select**  
 1-2: External, 6VAC/5VDC via PWR\_JACK  
 2-3: USB Power (May impact AK4584 performance)  
 NONE: No jumper if regulated 5V is supplied via CON1

**JP1: Microphone Select**  
 1-2: Route output to J1 Left  
 3-4: Route output to J1 Right

**Audio I/O (All Stereo)**  
 J1: Combo Optical/Analog input  
 J2: Combo Optical/Analog output  
 J3/J5/J7: Analog input  
 J2/J4/J6: Analog output

**SW1: General Purpose Switches**

Pos	Signal	DSP Pin
1	GPSW0	PF0
2	GPSW1	PF1
3	GPSW2	PF2
4	GPSW3	PF3
5	GPSW4	PF4
6	GPSW5	PF5
7	GPSW6	PD0
8	GPSW7	PD1

**CON1: Expansion Header**

1 +5V	20 +3.3V
3 GND	4 PAD1
5 PAD2	6 PAD3
7 PAD4	8 GPLED8
9 GPLED0	10 GPLED7
11 GPLED1	12 GPLED6
13 GPLED2	14 GPLED5
15 GPLED3	16 GPLED4
17 GPSW0	18 GPSW7
19 GPSW1	20 GPSW6
21 GPSW2	22 GPSW5
23 GPSW3	24 GPSW4
25 GND	26 GND

**SW2: Boot Mode Select**

Pos	Signal	For Pos.1-4:
1	MODA	ON = Logic 0
2	MODB	OFF = Logic 1
3	MODC	For Example, 110110 (654321) is Mode 9 with 8000 EE enabled
4	MODD	
5	BOOT EE SDA enable	
6	BOOT EE SCL enable	

**LED1-9: General Purpose LEDs**

ID	Signal	DSP Pin
LED1	GPLED0	PF7
LED2	GPLED1	PF8
LED3	GPLED2	PF9
LED4	GPLED3	PF10
LED5	GPLED4	TI00/PB0
LED6	GPLED5	TI01/PB1
LED7	GPLED6	PC2
LED8	GPLED7	PC5
LED9	GPLED8	PC6

**SW3: System/Board RESET**

+ PC7